PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED Q.A.	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/574653		257 438	199	2823	H. Lee

APPLICANTS

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Shawn Walsh

TITLE

A tunable sidewall spacer process for cmos integrated circuits

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)		
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)	

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☒ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner)   (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

ISSUE FEE

Amount Due	Date Paid

(Primary Examiner)   (Date)

ISSUE BATCH NUMBER

☐ The terminal ____ months of this patent have been disclaimed.

(Legal Instruments Examiner)   (Date)

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